TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSIII)

2SK1382

Relay Drive, Motor Drive and DC-DC Converter Applications

4-V gate drive

• Low drain-source ON resistance : $R_{DS(ON)} = 15 \text{ m}\Omega \text{ (typ.)}$

• High forward transfer admittance : |Y_{fs}| = 47 S (typ.)

• Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 100 V)

• Enhancement mode : $V_{th} = 0.8$ to 2.0 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	100	V
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	100	٧
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	٦	60	< <u>\</u>
	Pulse (Note 1)	IDP (240	A
Drain power dissipation	n (Tc = 25°C)	PB	200	w
Channel temperature		((T _{ch}	150	//°c
Storage temperature ra	ange	T _{stg}	-55 to 150	\$ }

Unit: mm

20.5 max

43.3±0.2

2.5

3.0

1.0 - 0.25

5.45±0.15

5.45±0.15

2.DRAIN (HEAT SINK)

3.SOURCE

JEDEC

JEITA

TOSHIBA

2-21F1B

Weight: 9.75 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

	1/		
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	0.625	°C/W
Thermal resistance, channel to ambient	R _{th} (ch-a)	35.7	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

This transistor is an electrostatic-sensitive device.

Please handle with caution.

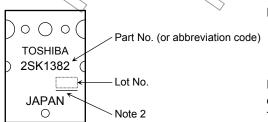
Electrical Characteristics (Ta = 25°C)

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±20 V, V _{DS} = 0 V	_	_	±100	nA
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	100	_	_	٧
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.0	V
Drain-source ON resistance		R _{DS} (ON)	V _{GS} = 4 V, I _D = 30 A	(F) 20	29	- mΩ
			V _{GS} = 10 V, I _D = 30 A	\nearrow	15	20	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 30 A	30	47	_	S
Input capacitano	:e	C _{iss}		_	7000	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	<u> </u>	400		pF
Output capacitance		Coss		_	2700		
Switching time Fall	Rise time	t _r	V_{GS}^{10V} V_{OUT} V_{OUT} V_{OUT} V_{OUT}	(16	<i>∕</i> 1 <i>⁄</i>	- ns
	Turn-on time	t _{on}			55) –	
	Fall time	t _f	4		80	_	
	Turn-off time	t _{off}	$V_{DD} = 50V$ $Duty = 1\%, t_{W} = 10\mu s$		280	_	
Total gate charg plus gate-drain)		Qg		_	176	_	
Gate-source cha	arge	Q _{gs} /	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 60 \text{ A}$	_	132	_	nC
Gate-drain ("mil	ler") charge	Q _{gd}		_	44	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current	IDR	<u> </u>	_	_	60	Α
Pulse drain reverse current (Note 1)) IDRP	_	_	_	240	Α
Forward voltage (diode)	V_{DSF}	I _{DR} = 60 A, V _{GS} = 0 V	_	_	-1.6	V
Reverse recovery time	t _{rr}	I _{DR} = 60 A, V _{GS} = 0 V dI _{DR} / dt = 50 A / μs		300	_	ns
Reverse recovered charge	Qrr		_	0.75	_	μC

Marking

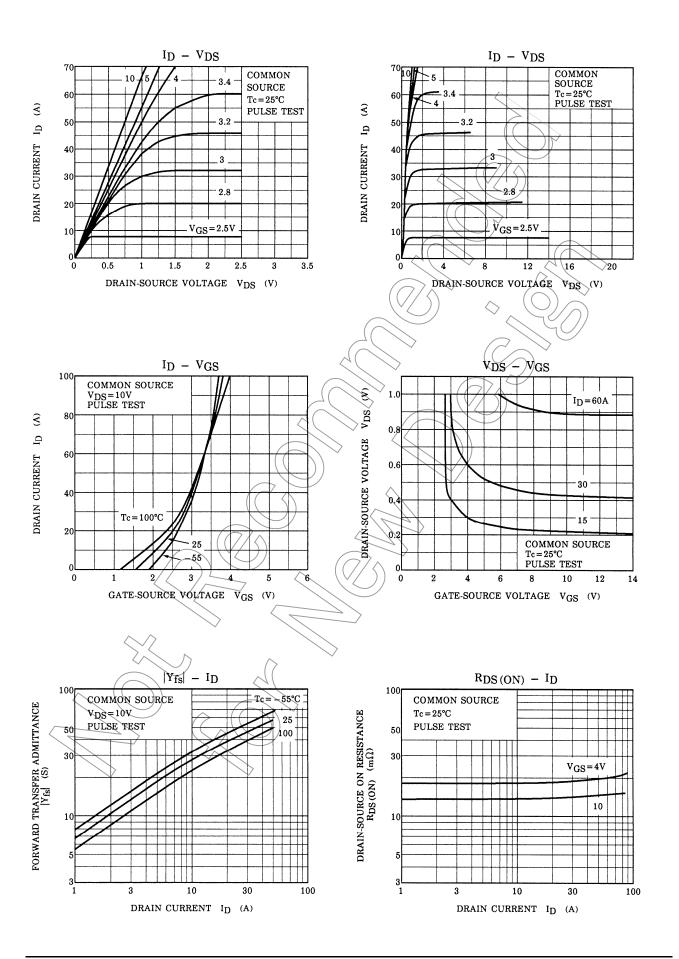


Note 2: A line under a Lot No. identifies the indication of product Labels.

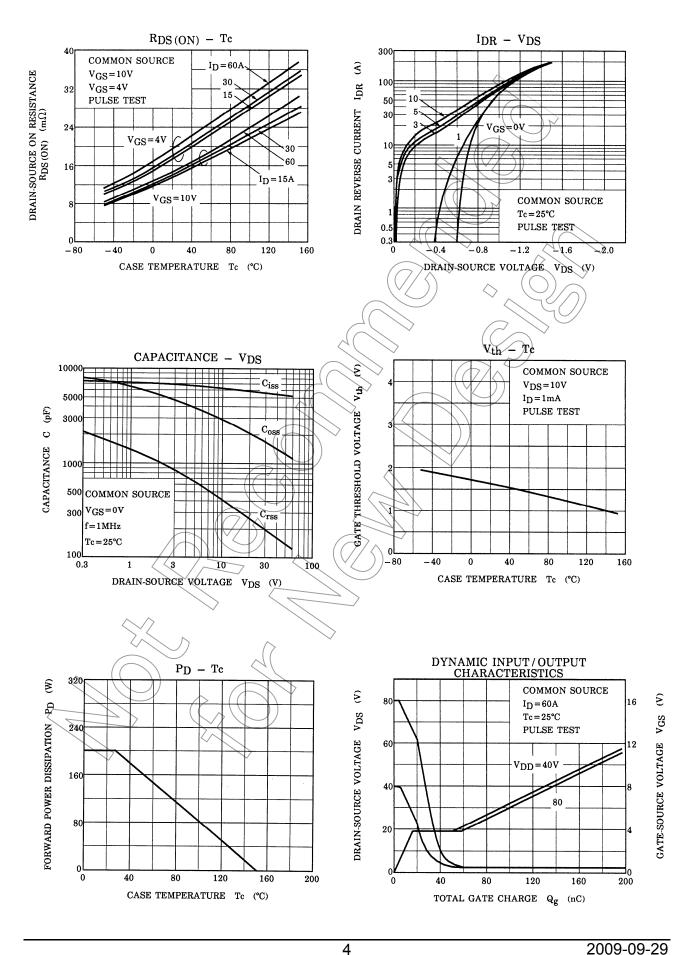
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

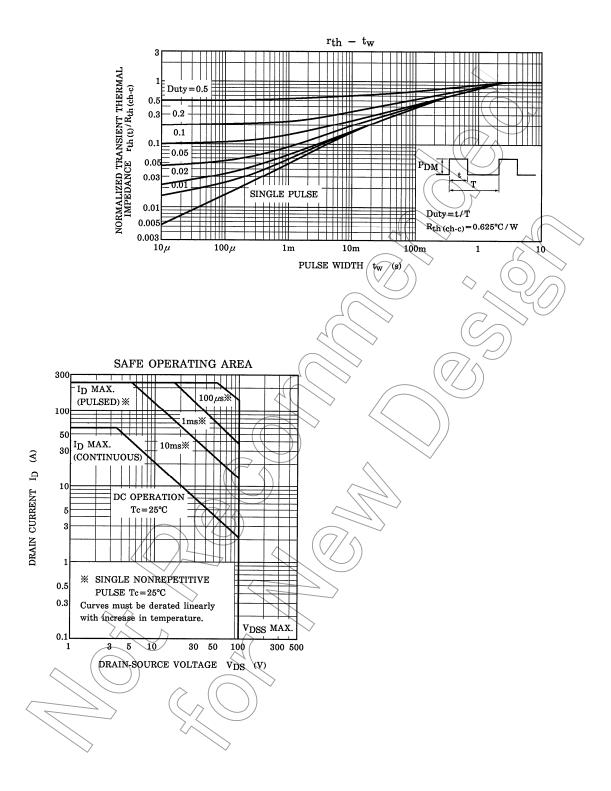
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